Method for correcting luminance and chrominance defects of a matrix display and matrix display and circuit for carrying out the method

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Cited documents:

WO9115923
EP0595649
US5315378
EP0448480
EP0402137
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Abstract of **EP0757498**

The method involves using a circuit containing three memories for primary colour correction look-up tables (LUT) and multipliers for corresponding inputs from a colour decoder. Correction is performed on a pixel to pixel basis. The multiplication products are processed by individual peak limiters (4-6). Unitary correction is applied to the slightly altered part of the picture and multiplied by a coefficient corresponding to the average of the look-up table contents over a number of preceding pixels. Smoothing can be applied to preserve detail threatened by chromatic overcorrection.

Fig. 3